# Generating a Configuration PROM File

The bitstream file is the result of the FPGA compile process. However that isn’t the file to be programmed into a serial configuration PROM – a further translation stage is needed.

To generate a configuration PROM file ready to program into the configuration memory device, follow these steps. We use the “BIN” format.

1. Open Vivado
2. Open the Saturn project file
3. Select tools > generate memory configuration file
4. Select “memory part”
5. Select manufacturer to **Spansion**
6. Select **s25fl256sxxxxxx0-spi-x1\_x2\_x4**
7. Set filename to “saturnprom.bin” in the project folder
8. Select “Interface” to **SPIx1**
9. Select **Load Bitstream files**
10. Select the project .bit file (normally in in a runs\impl folder)
11. Select **Overwrite**
12. Click **OK**

A screenshot of a computer

Description automatically generated

That will only take a few seconds. Then copy that file to your Raspberry pi; execute “flashwriter” app, load that file as the Primary image into the configuration PROM. Shut down, power off and then power on your Saturn.

There is a different procedure to create the fallback image; see the main FPGA design description document for details.